1. Any Interrupt control line goes high; set DF flag high.
2. Finish current cycle. (When DF and IF are high the interrupt sequence will be executed on the next cycle.)
3. Push FR to return stack.
4. Push PC to return stack.
5. Send Acknowledgement signal to bus device that sent interrupt request. (There is an interrupt and an acknowledgement control line for each device.)
6. Receive interrupt code from device (via GPI/O).
7. Jump to interrupt vector that corresponds to code. (7DF00h to 7DFFFh)
8. Execute this code as per usual.
9. When finished with execution, exit (somehow)… Set DF low.
10. Pop PC from stack.
11. Pop FR from stack.
12. Resume normal execution.
13. Interrupt control line goes high; latch DF high.
14. Save 3bit interrupt code in temporary register; finish executing current cycle.
15. If IF and DF are both high begin sequence, else set DF low and finish.
16. Push PC to stack.
17. Jump to corresponding interrupt vector (0b 111 1111 1111 xxx0 0000).
18. Execute code; when DF goes low stop (flags “DF low” instruction).
19. Pop PC from stack.
20. Resume normal execution.